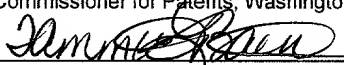


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Pete Joseph Procopio)
Serial No.: 09/899,486)
Filing Date: July 5, 2001)
Docket No.: 013777-004) Art Unit: 2643
Title: METHOD AND APPARATUS FOR)
REPORTING TOTAL CALL QUALITY)

Commissioner for Patents
Washington, D.C. 20231

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail on the date indicated in an envelope addressed to: Commissioner for Patents, Washington, DC 20231	
	08-30-01
Printed Name: TAMMIE L. BAIN	Date

Sir:

PRELIMINARY AMENDMENT PURSUANT TO 37 C.F.R. § 1.115

This paper is being filed before issuance of the first Office Action. Please amend, without prejudice, the above-identified application as follows:

IN THE SPECIFICATION

Please replace the paragraph beginning at page 5, line 1, and ending at line 2, with the following rewritten paragraph:

FIGs. 2A and 2B illustrate a flow diagram of the preferred embodiment for determining the total call quality.

Please replace the paragraph beginning at page 5, line 5, and ending at line 5, with the following rewritten paragraph:

FIGs. 4A and 4B illustrate a block diagram of the components of a host computer.

Please replace the paragraph beginning at page 7, line 6, and ending at line 19, with the following rewritten paragraph:

Referring now to FIGs. 4A and 4B, a block diagram of the components of the host computer 414 shown in FIG. 3 is illustrated. The system unit 302 includes a bus or a plurality of buses 402 to which various components are coupled and by which communication between the various components is accomplished. The processor 404 is connected to the bus 402 and is supported by read only memory (ROM) 406 and random access memory (RAM) 408 also connected to the bus 402. The ROM 406 contains among other software code the Basic Input-Output system (BIOS) which controls basic hardware operations such as the interaction and the disk drives and the keyboard. The RAM 408 is the main memory into which the operating system and application programs are loaded. The memory management chip 410 is connected to the bus 402 and controls direct memory access operations including, passing data between the RAM 408 and hard disk drive 412 and floppy disk drive 414. The CD ROM 416 also coupled to the bus 402 is used to store a large amount of data, e.g., a multimedia program or presentation.

REMARKS

This responds to a Notice of Omitted Item(s) in a Nonprovisional Application mailed on May 31, 2001.

In the notice, it is stated that Figure 4, as described in the specification, appears to have been omitted from the application. The reference to Figure 4 is an inadvertent error. Figure 4 should not be referenced in the specification. Rather, every reference to Figure 4 in the specification should be a reference to Figures 4A and 4B. The references to Figure 4 in the specification is an inadvertent error which is corrected by this amendment.

Therefore, the specification has been amended and corrected throughout the application to clarify and consistently refer to Figures 4A and 4B rather than Figure 4. Applicant respectfully submits that the correction of the references to the drawings in the specification adds no new matter to the present application.

Finally, Applicant corrected a typographical error in the paragraph beginning at page 5, line 1, and ending at line 2, by changing "FIG." to "FIGs."

Applicant respectfully requests the entry and approval of the foregoing amendments prior to examination.

No amendment made was related to the statutory requirements of patentability unless expressly stated herein.

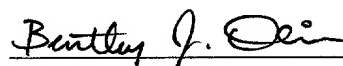
No amendment made was for the purpose of narrowing the scope of any claim, unless Applicant has argued herein that such amendment was a narrowing amendment made to distinguish over a specified reference or references.

The Commissioner is hereby authorized to charge payment of any additional filing or application fees associated with this communication or credit any overpayment to Deposit Account No. 13-4365.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned "**Version with markings to show changes made.**"

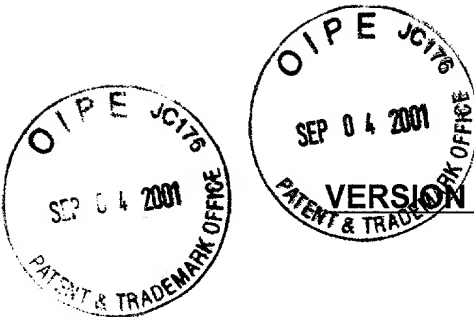
If there are any questions regarding these amendments, please do not hesitate to contact us.

Respectfully submitted,



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Dated: August 27, 2001



VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE SPECIFICATION

The paragraph beginning at page 5, line 1, and ending at line 2, has been amended as follows:

FIGs. 2A and 2B illustrate a flow diagram of the preferred embodiment for determining the total call quality.

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the bus 402 and is supported by read only memory (ROM) 406 and random access memory (RAM) 408 also connected to the bus 402. The ROM 406 contains among other software code the Basic Input-Output system (BIOS) which controls basic hardware operations such as the interaction and the disk drives and the keyboard. The RAM 408 is the main memory into which the operating system and application programs are loaded. The memory management chip 410 is connected to the bus 402 and controls direct memory access operations including, passing data between the RAM 408 and hard disk drive 412 and floppy disk drive 414. The CD ROM 416 also coupled to the bus 402 is used to store a large amount of data, e.g., a multimedia program or presentation.